Bharamu S K

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# PROFILE

I graduated From **J.S.S.A.T.E, Bengaluru**. **Experienced in Embedded Software Engineering and trained** in

**Blended Advanced Design and Veriﬁcation in Maven Silicon**. Passionate about technology and coding.

# PROFESSIONAL EXPERIENCE

**Senior Embedded Engineer, M.S Technology Bangalore** Provides solutions and innovation for Energy Management and communication.

## Achievements/Tasks:

* To Design, Develop, Implement and test the Embedded Software and Hardware.
* Strong knowledge of communication protocol **UART, I2C, RS232, RS485, SPI**
* Designed and developed the electronic zig for testing of PCB.

## Tool Expertise: Atollic | Arduino | ESP-IDF | Code Compos Studio | Altium | Kicad | OrCad.

* Implementation of **TCP/IP**, **MQTT** protocols in devices
* Generating reports, technical manuals, and software development documentation.
* Designing a PCB

# PROFESSIONAL TRAINING

## Advance Design and Veriﬁcation training

Maven Silicon Bangalore

**Embedded Systems Trainee,** *Cranes varsity a Training Division of Cranes Software International Ltd Bangalore*

# EDUCATION

## Electronics and Communication Engineering.

J.S.S.Accademy of Technical Education Bangalore

## Diploma in Electronics and Communication Engineering

B.V.V.S Polytechnic Bagalkot

## Secendory Education

S.S.S.B.V.V.S Hi-School Halingali

# SKILLS

Jul 2019 – Dec 2022

Bangalore, India

May 2022 – present

Bangalore

Sep 2018 – May 2019

Bangalore

Jul 2015 – Jun 2018

Jul 2012 – May 2015

Apr 2012



Digital Electronics | Verilog | Sysytem Verilog | SVA | UVM | OOPS Concept | STA | Perl | Embededded C.

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# TOOLS

Questasim | Modelsim | Quartus Prime |EDA Playground | Linux.

# PROJECTS

## Router 1x3 Design and veriﬁcation

The router accepts data packets on a single 8-bit port and routes them to one of the three output channels - channel0,channel1, and channel2.

## Responsibilities :

* Architected the block-level structure for the design.
* Implemented RTL using Verilog HDL
* Veriﬁed the RTL model using the system Verilog
* Synthesized the design

## Energy Meter Reading Using Wi-Fi and BLE

Designed and developed an end node to communicate with the meter using UART and then send the data to the gateway through Wiﬁ or BLE. Gateway uses 4G /2G module to communicate with head end system

**RDPR,** *Rural Development Program*

Built an End-node used for communication and control the water tank level, valve control,3 Phase motor Starter startup control, and gateway for communicating with the Server.

## GAS and Water Meter

In this project, we collected gas and water meter data using an LC sensor or REED switch. and send data using RS485

## Smart Lock Dual Authentication

The project aims to enhance system security. We used RFID to unlock the system and 4 Digit Password for the next step authentication

## Automatic Speed Controller using relay and magnetic sensors

The objective of the project was to control vehicle speed in schools and hospital premises.

## PCB designs

* Designed END Node, IR Probe, 4G/3G/2G Wiﬁ-Gateway
* Designed 4:1 serial communication with an Energy meter.
* Designed Single Phase EV Charger for bikes.
* Designed Lora Node for Energy Meter Reading.
* Designed 3'Inch Thermal Printer for Energy Meter Bill Generation.

# LANGUAGES

Kannada Telugu

# HOBBIES

English Hindi

Playing Cricket | Kabaddi | Travelling | Watching Movies

# DECLARATION

I, hereby declare that the information furnished above is correct to the best of my knowledge.



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Bangalore